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## \*BIBDATASHEET\*

**CONFIRMATION NO. 4905** 

Bib Data Sheet

SERIAL NUMBE 10/823,664	FILING OR 371(c) DATE 04/14/2004 RULE	DATE         CLASS         GROUP AF           04/14/2004         365         2824		JP ART 2824	JNIT ATTORNEY DOCKET NO. XA-9665A				
Hiroshi Akasaki, Ome, JAPAN; Shuichi Miyaoka, Hannou, JAPAN; Yuji Yokoyama, Ome, JAPAN; Masatoshi Hasegawa, Ome, JAPAN; Kozaburo Kurita, Ome, JAPAN; This application is a DIV of 10/153,525 05/24/2002 PAT 6,735,129  ** FOREIGN APPLICATIONS ************************************									
Foreign Priority claime 35 USC 119 (a-d) cond met Verified and	fter STATE OR COUNTRY JAPAN	DRAN	SHEETS TOT DRAWING CLA 12			INDEPENDENT CLAIMS 1			
Acknowledged Examiner's Signatúre Initials  ADDRESS  181									
TITLE SEMICONDUCTOR INTEGRATED CIRCUIT HAVING PROGRAMMABLE DELAYS FOR GENERATING TIMING SIGNALS WITH TIME DIFFERENCE BEING NON-INTEGRAL MULTIPLE OF CLOCK CYCLE									
			All Fees						
RECEIVED				1.16 Fees (Filing)					
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					Other				
1						☐ Credit			